

## Features

- Fast Switching
- Low Gate Charge and  $R_{DS(on)}$
- Low Reverse transfer capacitances



## Product Summary

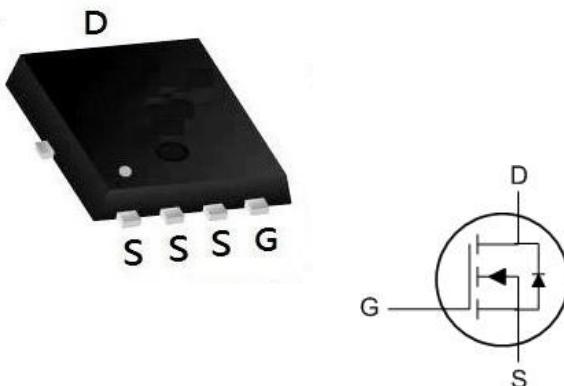
BVDSS	RDS(on)	ID
120V	7.7mΩ	80A

## Applications

- DC-DC converter
- Portable Equipment
- Power management

**100% DVDS Tested**  
**100% Avalanche Tested**

## PDFN5060-8L Pin Configuration



## Absolute Maximum Ratings:

Symbol	Parameter	Value	Units
$V_{DSS}$	Drain-to-Source Voltage	120	V
$I_D$	Continuous Drain Current $T_C = 25^\circ C$	80	A
	Continuous Drain Current $T_C = 100^\circ C$	49	A
$I_{DM}^{a1}$	Pulsed Drain Current	280	A
$E_{AS}^{a2}$	Single pulse avalanche energy	300	mJ
$V_{GS}$	Gate-to-Source Voltage	$\pm 20$	V
$P_D$	Power Dissipation	105	W
$T_J, T_{STG}$	Operating Junction and Storage Temperature Range	150, -55 to 150	°C
$T_L$	Maximum Temperature for Soldering	260	°C

## Thermal Characteristics:

Symbol	Parameter	Value	Units
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	1.19	°C/W

**Electrical Characteristics (Tc= 25°C unless otherwise specified) :**

Static Characteristics						
Symbol	Parameter	Test Conditions	Value			Units
			Min.	Typ.	Max.	
V <sub>DSS</sub>	Drain to Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	120	--	--	V
I <sub>DSS</sub>	Drain to Source Leakage Current	V <sub>DS</sub> = 120V, V <sub>GS</sub> = 0V	--	--	1	μA
I <sub>GSS(F)</sub>	Gate to Source Forward Leakage	V <sub>GS</sub> =+20V	--	--	100	nA
I <sub>GSS(R)</sub>	Gate to Source Reverse Leakage	V <sub>GS</sub> =-20V	--	--	-100	nA
V <sub>GS(TH)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> = 250μA	1.4	1.8	2.2	V
R <sub>DS(ON)</sub>	Drain-to-Source On-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	--	7.7	9.5	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =10A	--	9	11	mΩ

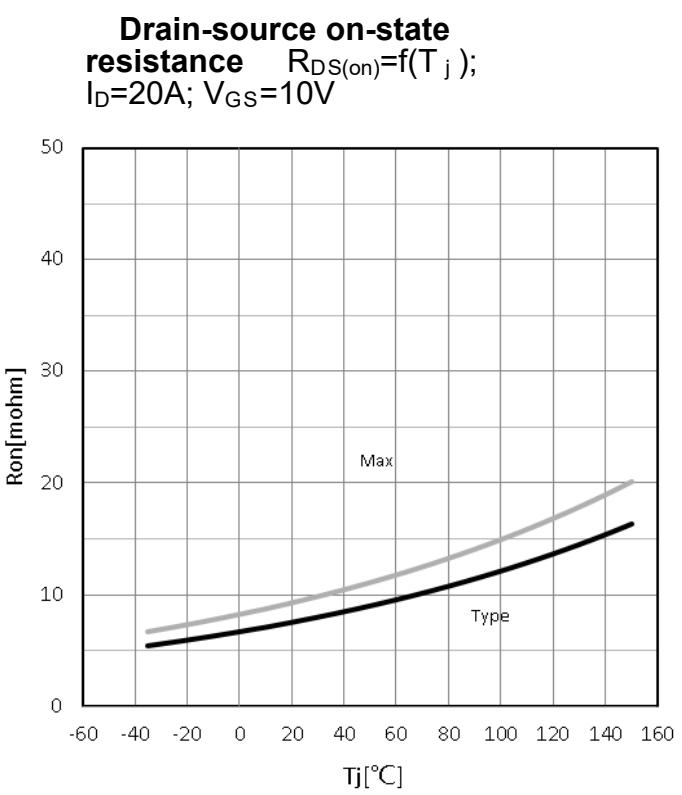
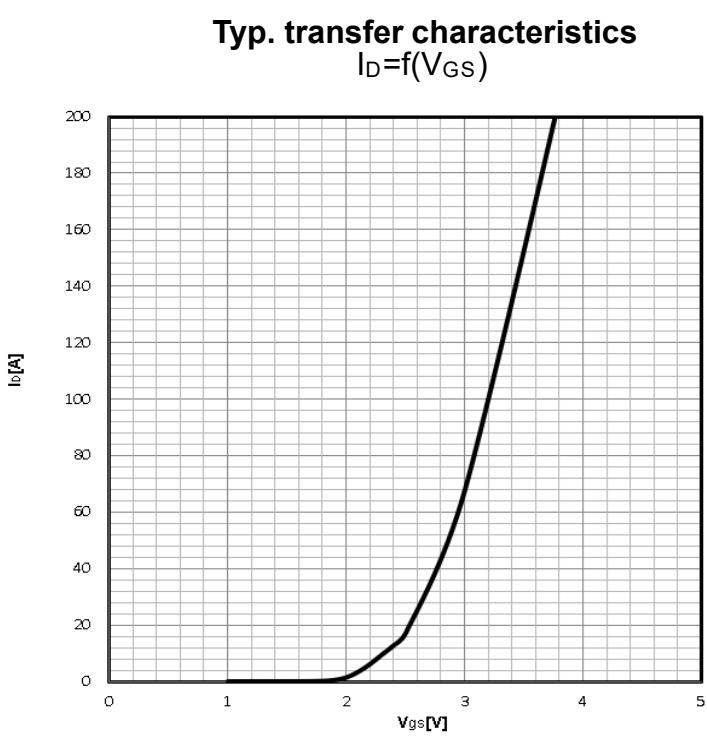
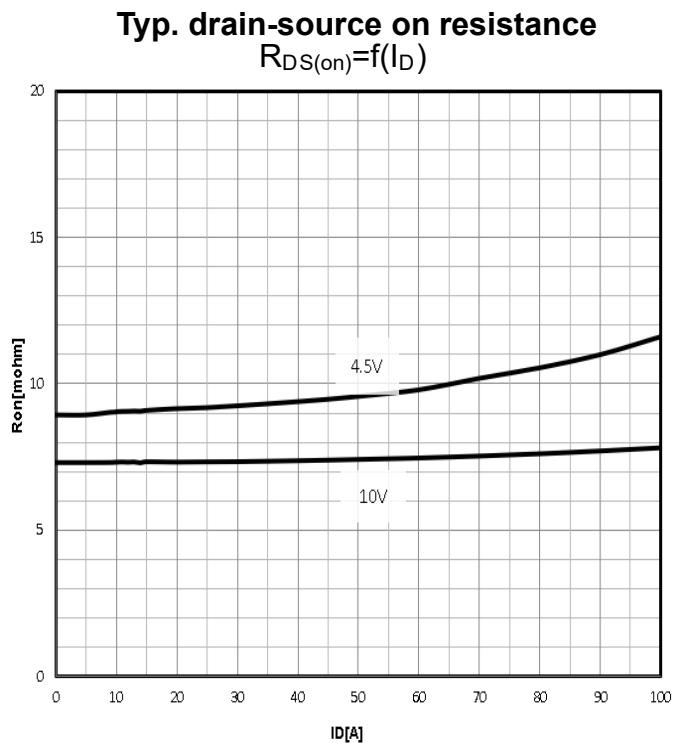
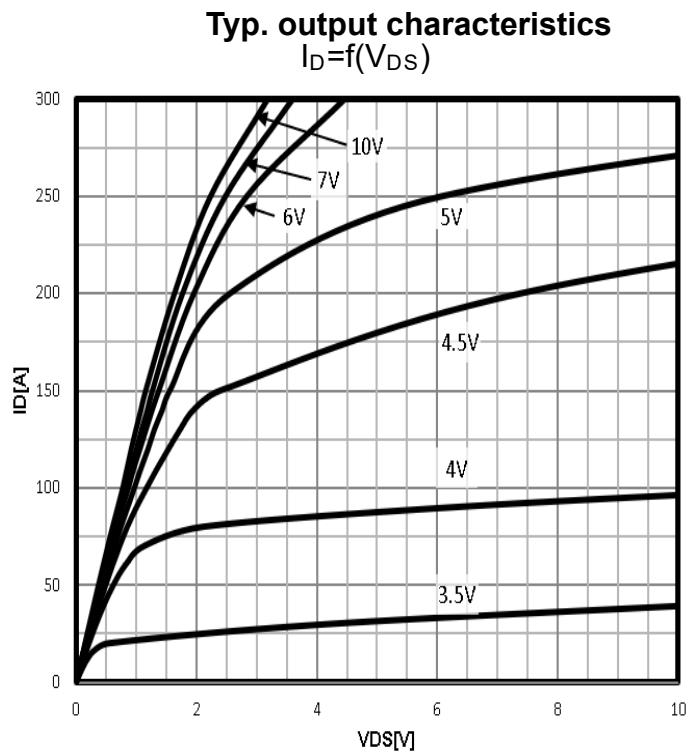
Dynamic Characteristics						
Symbol	Parameter	Test Conditions	Value			Units
			Min.	Typ.	Max.	
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> = 0V V <sub>DS</sub> = 60V f = 1.0MHz	--	2410	--	pF
C <sub>oss</sub>	Output Capacitance		--	282	--	
C <sub>rss</sub>	Reverse Transfer Capacitance		--	8	--	

Resistive Switching Characteristics						
Symbol	Parameter	Test Conditions	Value			Units
			Min.	Typ.	Max.	
t <sub>d(ON)</sub>	Turn-on Delay Time	I <sub>D</sub> =20A V <sub>DS</sub> = 60V V <sub>GS</sub> = 10V R <sub>G</sub> = 5Ω	--	20	--	ns
t <sub>r</sub>	Rise Time		--	15	--	
t <sub>d(OFF)</sub>	Turn-Off Delay Time		--	32	--	
t <sub>f</sub>	Fall Time		--	10	--	
Q <sub>g</sub>	Total Gate Charge	V <sub>GS</sub> =0~10V V <sub>DS</sub> = 60V I <sub>D</sub> =20A	--	41	--	nC
Q <sub>gs</sub>	Gate Source Charge		--	12	--	
Q <sub>gd</sub>	Gate Drain Charge		--	10	--	

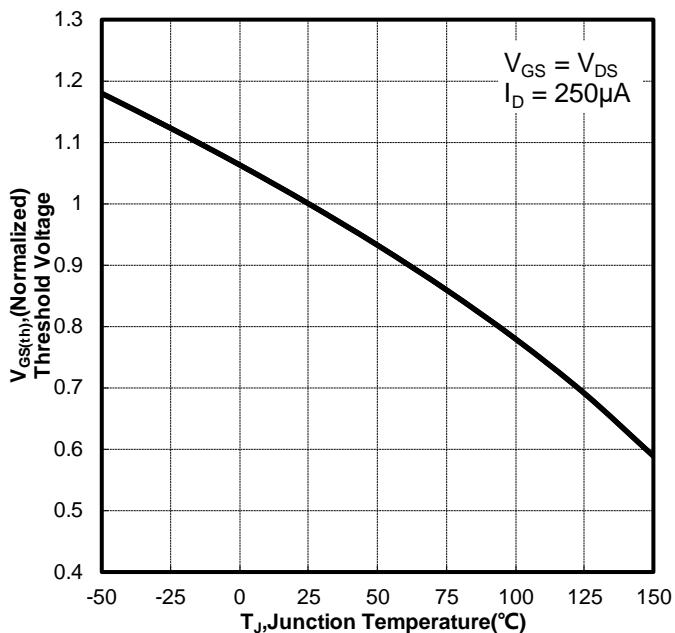
Source-Drain Diode Characteristics						
Symbol	Parameter	Test Conditions	Value			Units
			Min.	Typ.	Max.	
I <sub>s</sub>	Diode Forward Current	T <sub>c</sub> =25 °C	--	--	80	A
V <sub>SD</sub>	Diode Forward Voltage	I <sub>s</sub> =20A, V <sub>GS</sub> =0V	--	0.83	1.2	V
t <sub>rr</sub>	Reverse Recovery time	I <sub>s</sub> =40A, dI/dt=100A/μs	--	65	--	ns
Q <sub>rr</sub>	Reverse Recovery Charge		--	109	--	nC

a<sup>1</sup>: Repetitive rating; pulse width limited by maximum junction temperature

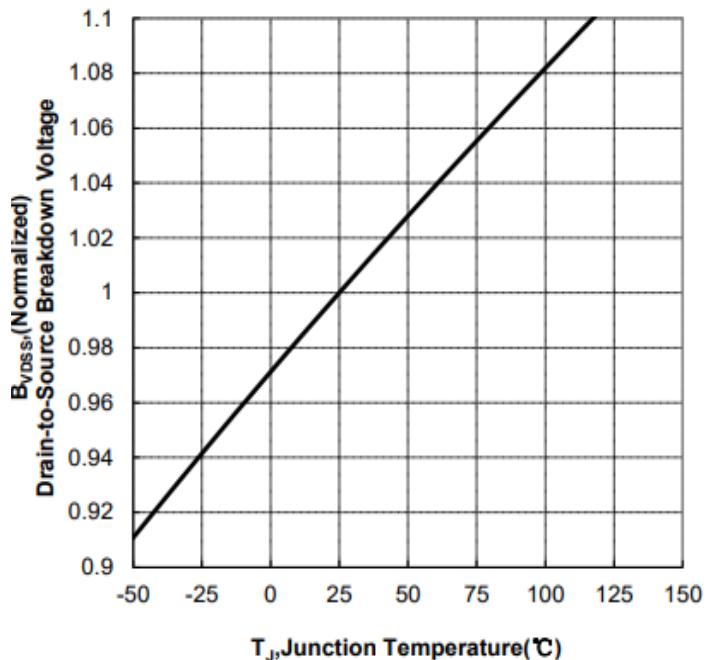
a<sup>2</sup>: VDD=60V, L=0.5mH, Rg=25Ω, Starting TJ=25 °C

**Characteristics Curve:**


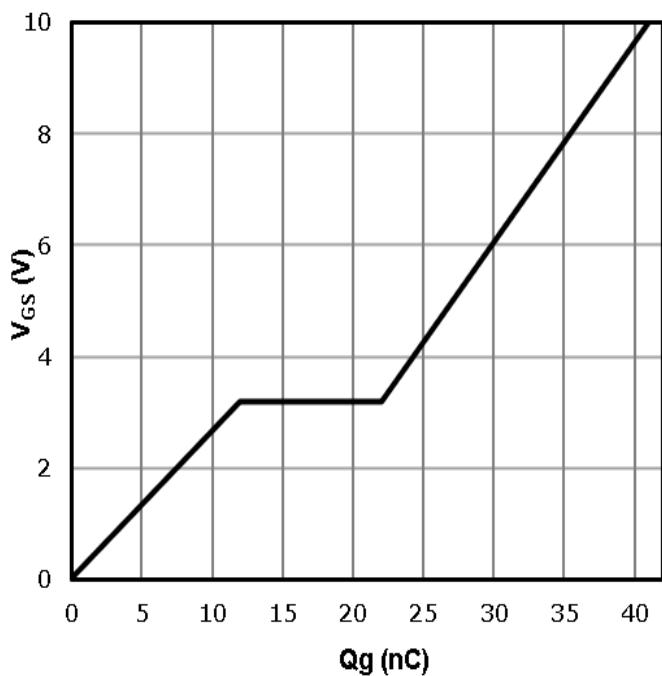
**Gate Threshold Voltage**  
 $V_{TH}=f(T_j)$ ;  
 $I_D=250\mu A$



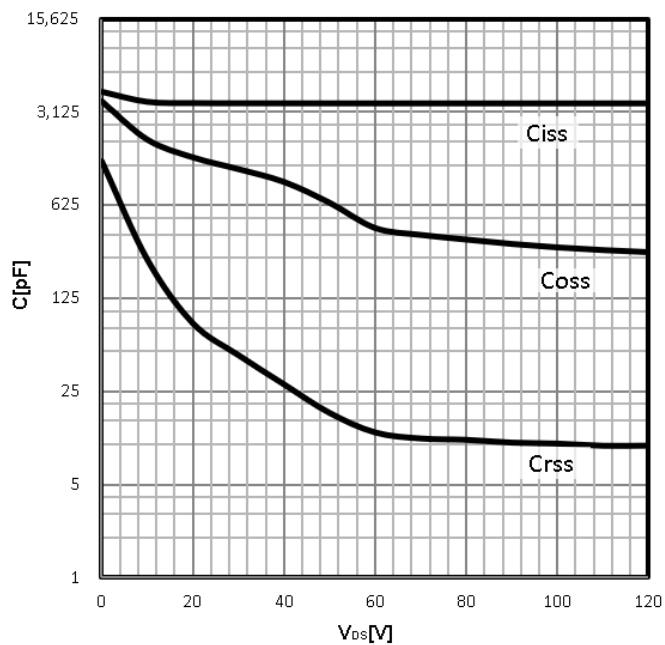
**Drain-source breakdown voltage**  
 $V_{BR(DSS)}=f(T_j)$ ;  $I_D=250\mu A$



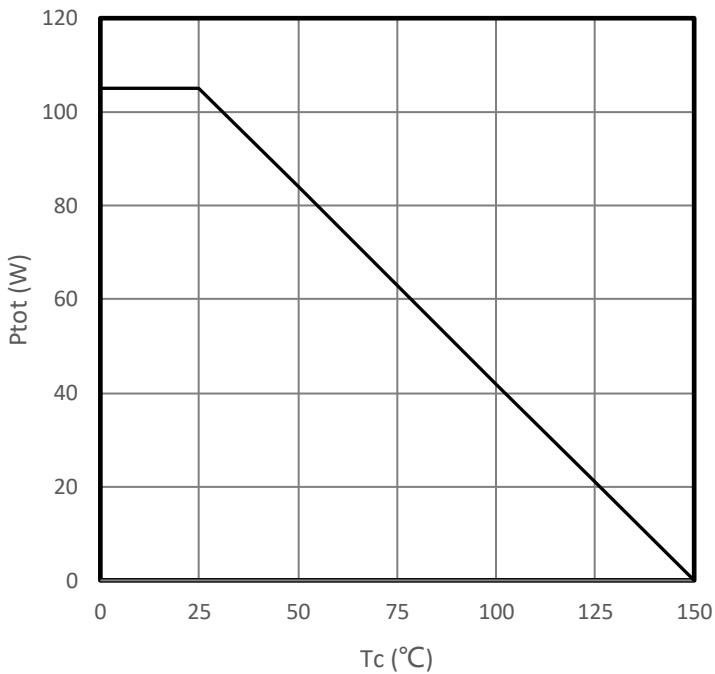
**Typ. gate charge**  
 $V_{GS}=f(Q_{gate})$



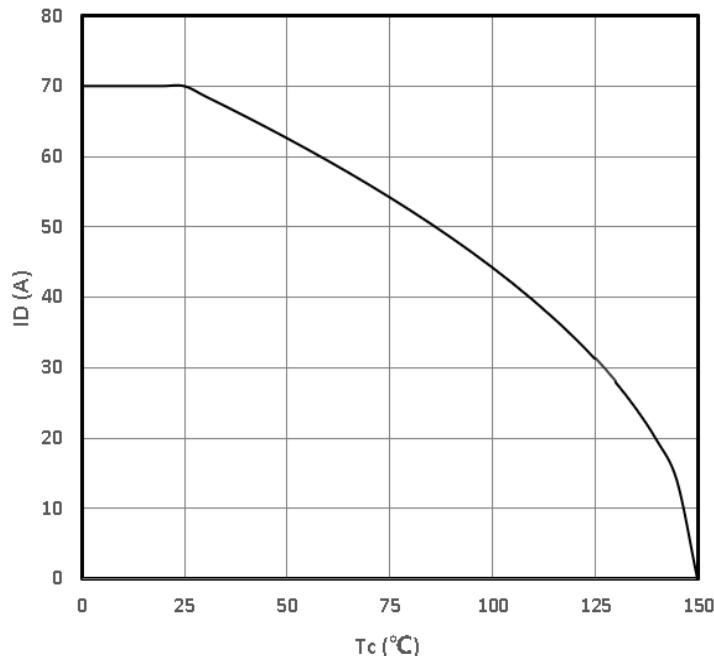
**Typ. capacitances**  
 $C=f(V_{DS})$ ;  $V_{GS}=0V$ ;  $f=1MHz$



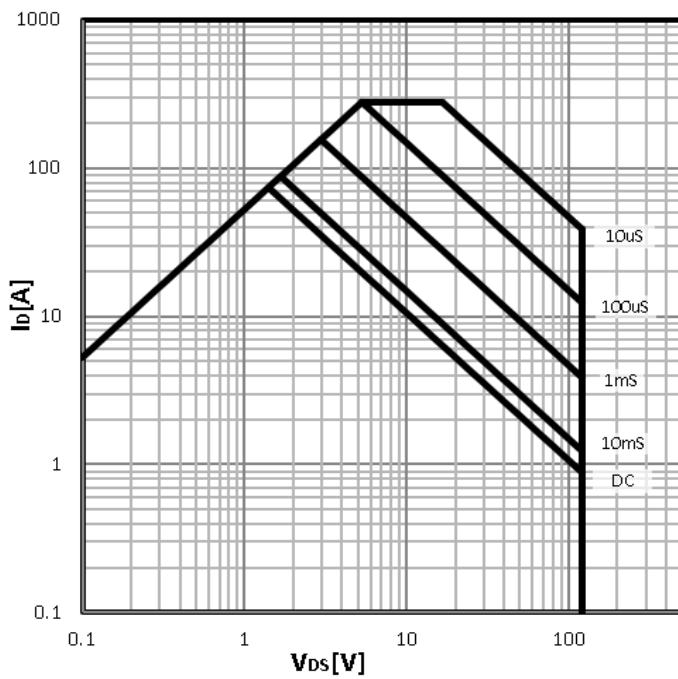
**Power Dissipation**  
 $P_{tot}=f(T_j)$



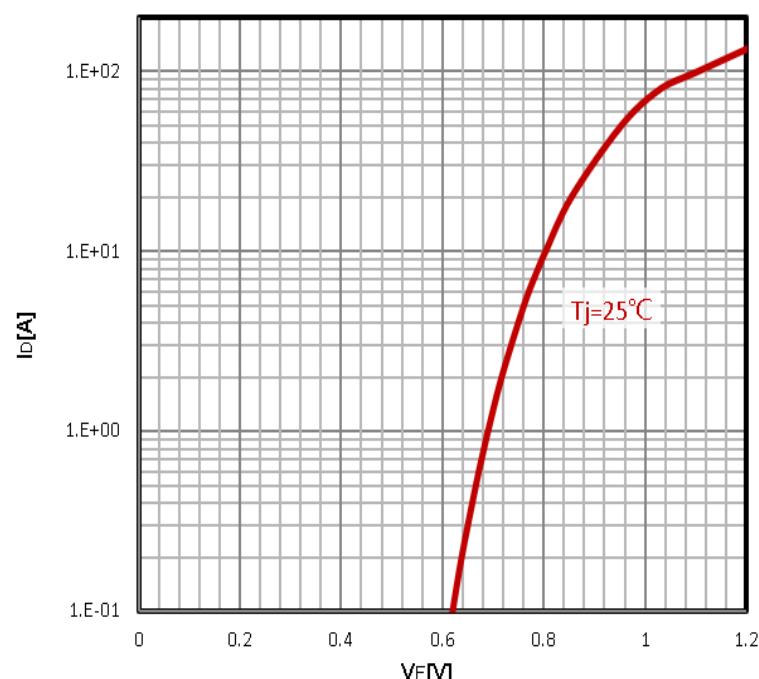
**Maximum Drain Current**  
 $I_D=f(T_c)$



**Safe operating area**  $I_D=f(V_{DS})$

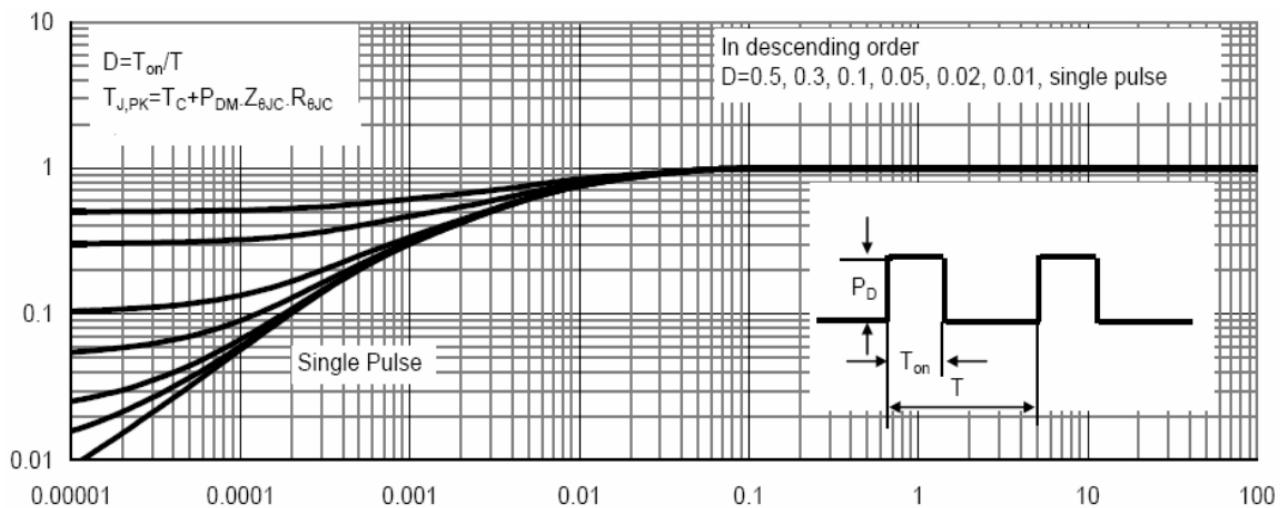


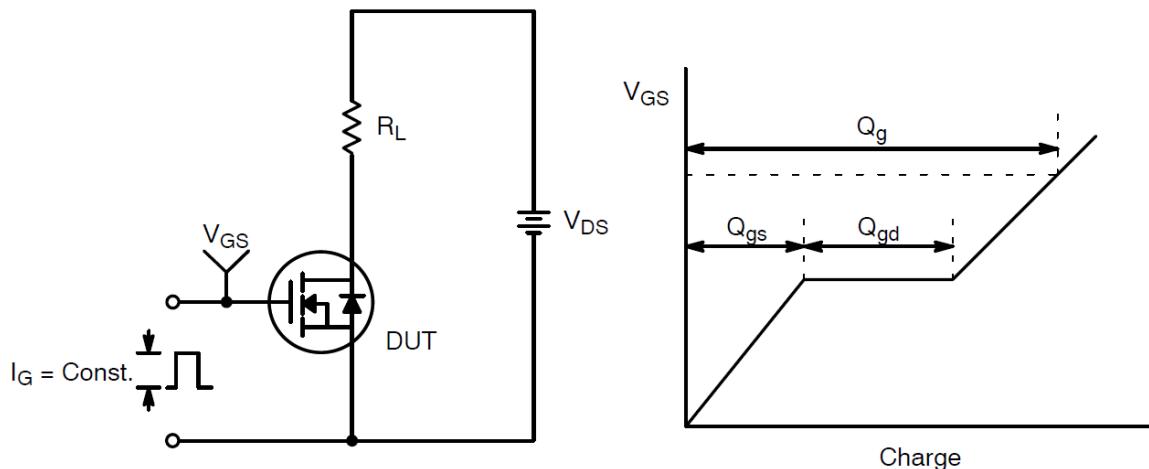
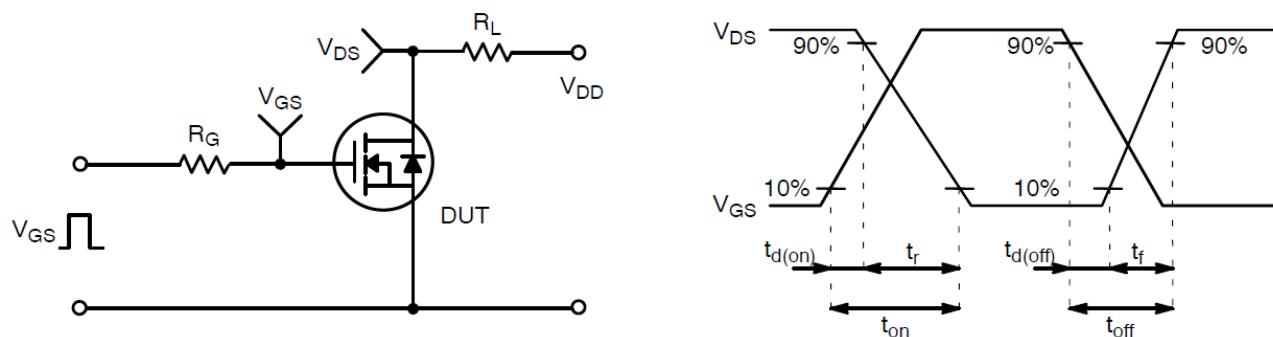
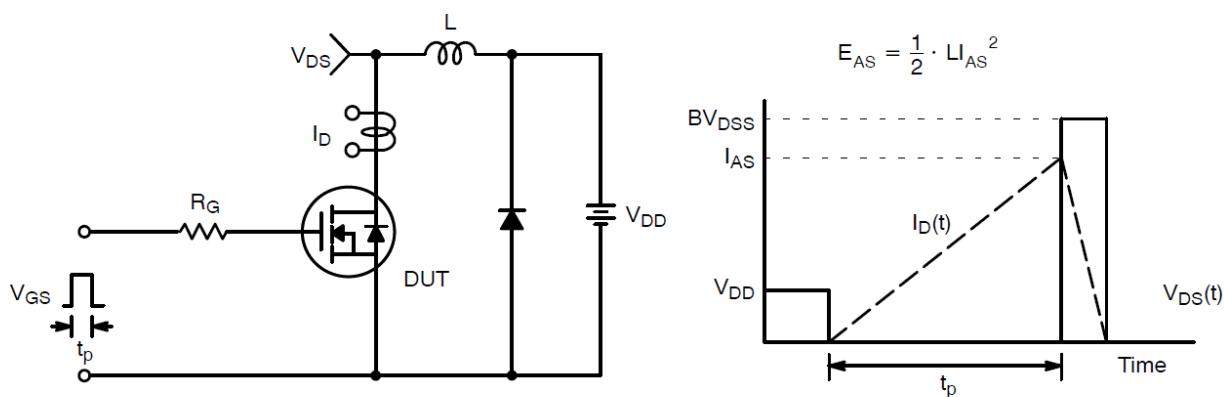
**Body Diode Forward Voltage Variation**  
 $I_F=f(V_{GS})$

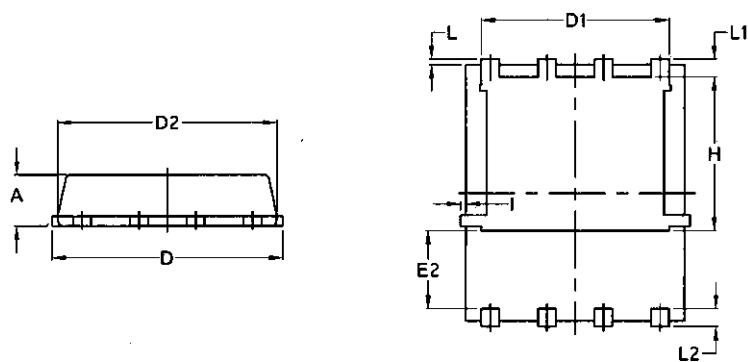
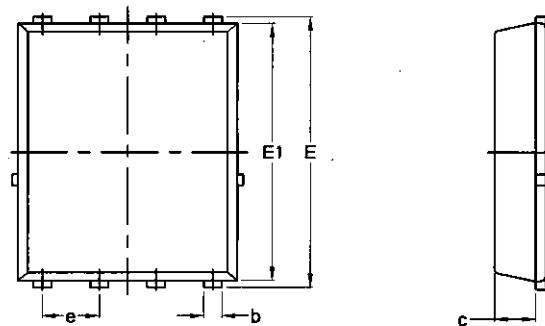


**Max. transient thermal impedance**

$$Z_{thJC} = f(t_p)$$



**Test Circuit and Waveform:**

**Gate Charge Test Circuit & Waveform**

**Resistive Switching Test Circuit & Waveforms**

**Unclamped Inductive Switching Test Circuit & Waveforms**

**Package Mechanical Data-PDFN5060-8L-Single**


Symbol	Common			
	mm		Inch	
	Min	Max	Min	Max
A	1.03	1.17	0.0406	0.0461
b	0.34	0.48	0.0134	0.0189
c	0.824	0.0970	0.0324	0.082
D	4.80	5.40	0.1890	0.2126
D1	4.11	4.31	0.1618	0.1697
D2	4.80	5.00	0.1890	0.1969
E	5.95	6.15	0.2343	0.2421
E1	5.65	5.85	0.2224	0.2303
E2	1.60	/	0.0630	/
e	1.27 BSC		0.05 BSC	
L	0.05	0.25	0.0020	0.0098
L1	0.38	0.50	0.0150	0.0197
L2	0.38	0.50	0.0150	0.0197
H	3.30	3.50	0.1299	0.1378
I	/	0.18	/	0.0070