



Features

- Split Gate Trench MOSFET technology
- Excellent package for heat dissipation
- High density cell design for low $R_{DS(ON)}$

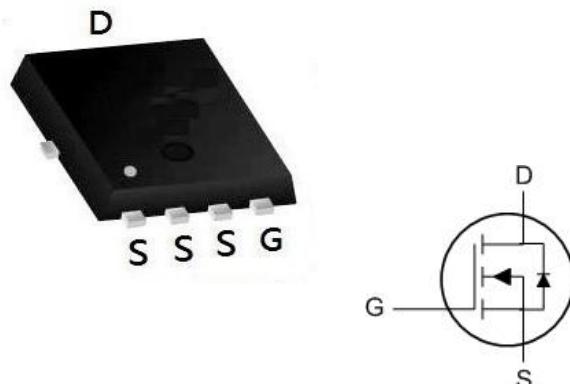
Product Summary

BVDSS	RDS(on)	ID
30V	0.8mΩ	230A

Applications

- DC-DC Converters
- Power management functions
- Synchronous-rectification applications

PDFN5060-8L(CLIP) Pin Configuration



Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	I_D	230	A
		100	
Pulsed Drain Current ¹	I_{DM}	400	A
Single Pulse Avalanche Energy ²	E_{AS}	156	mJ
Total Power Dissipation	P_D	93	W
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 150	°C

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance from Junction-to-Ambient ³	$R_{\theta JA}$	50	°C/W
Thermal Resistance from Junction-to-Case	$R_{\theta JC}$	1.35	°C/W

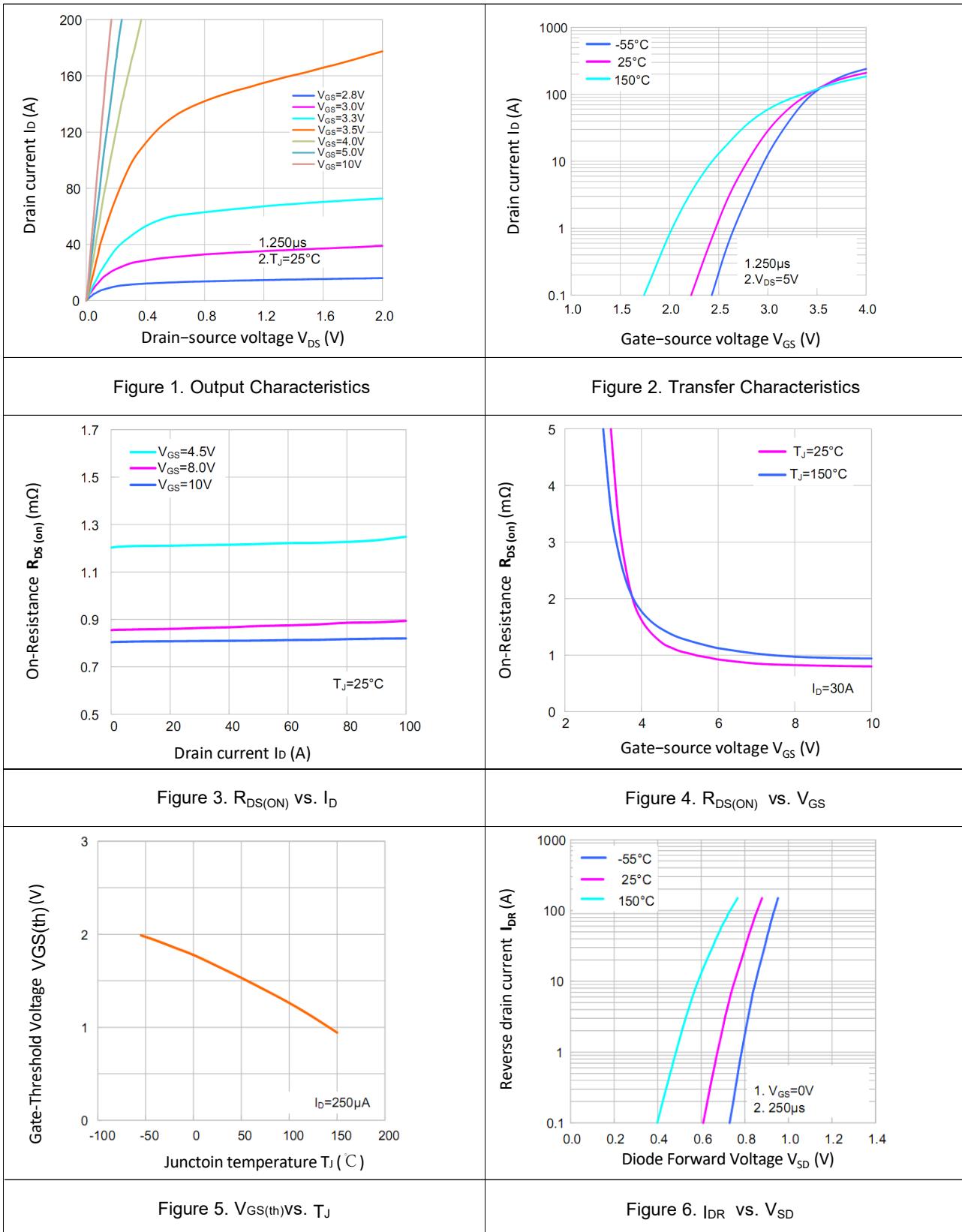
Electrical Characteristics ($T_J = 25^\circ\text{C}$, unless otherwise noted)

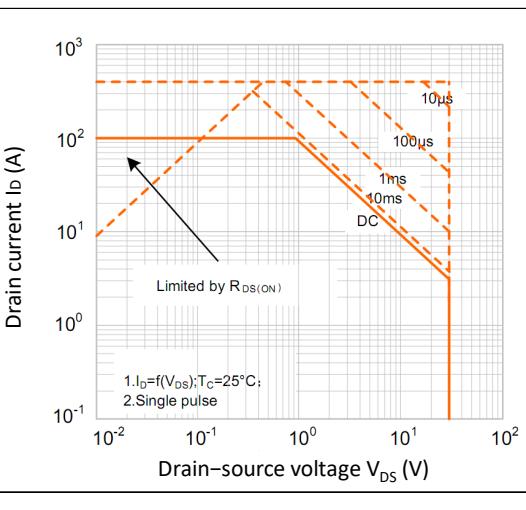
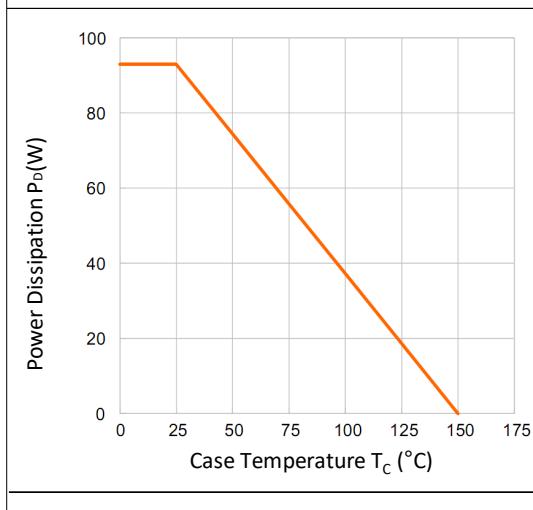
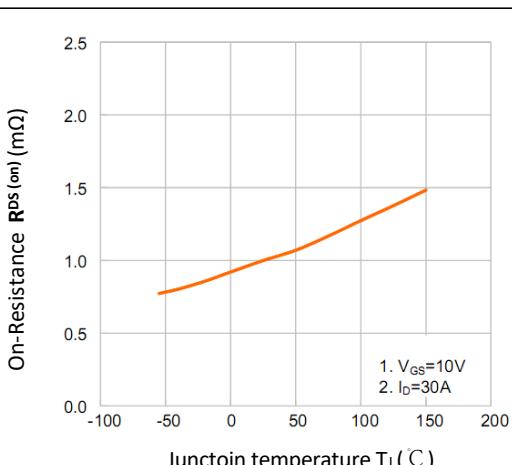
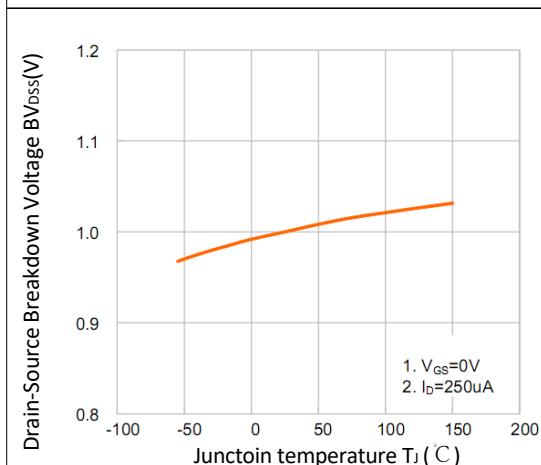
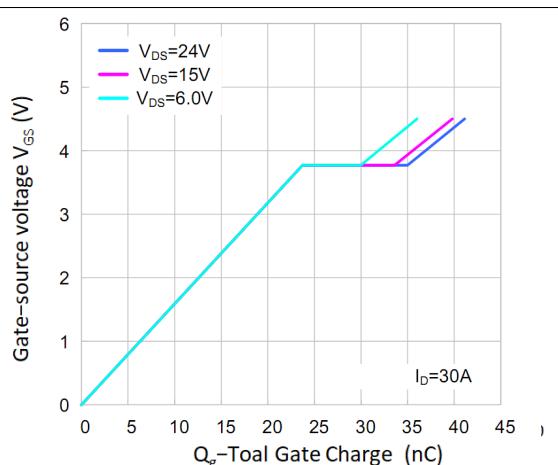
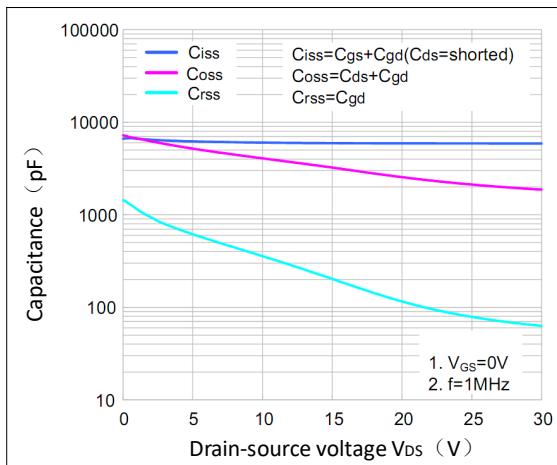
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	30	-	-	V
Gate-body Leakage Current	I_{GSS}	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = \pm 20\text{V}$	-	-	± 100	nA
Zero Gate Voltage Drain Current $T_J=25^\circ\text{C}$	I_{DSS}	$V_{\text{DS}} = 30\text{V}, V_{\text{GS}} = 0\text{V}$	-	-	1	μA
$T_J=125^\circ\text{C}$			-	1.5	-	
Gate-Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	1.3	-	2.3	V
Drain-Source On-Resistance ⁴	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10\text{V}, I_D = 20\text{A}$	-	0.8	1.1	$\text{m}\Omega$
		$V_{\text{GS}} = 4.5\text{V}, I_D = 10\text{A}$	-	-	-	
Forward Transconductance ⁴	g_{fs}	$V_{\text{DS}} = 10\text{V}, I_D = 20\text{A}$	-	130	-	S
Dynamic Characteristics⁵						
Input Capacitance	C_{iss}	$V_{\text{DS}} = 15\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$	-	5903	-	pF
Output Capacitance	C_{oss}		-	3216	-	
Reverse Transfer Capacitance	C_{rss}		-	204	-	
Gate Resistance	R_g	$f = 1\text{MHz}$	-	1.3	-	Ω
Switching Characteristics⁵						
Total Gate Charge	Q_g	$V_{\text{GS}} = 4.5\text{V}, V_{\text{DS}} = 15\text{V}, I_D = 30\text{A}$	-	40	-	nC
Gate-Source Charge	Q_{gs}		-	23	-	
Gate-Drain Charge	Q_{gd}		-	9.9	-	
Turn-On Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{GS}} = 4.5\text{V}, V_{\text{DD}} = 15\text{V}, R_G = 3\Omega, I_D = 15\text{A}$	-	43	-	ns
Rise Time	t_r		-	80	-	
Turn-Off Delay Time	$t_{\text{d}(\text{off})}$		-	52	-	
Fall Time	t_f		-	32	-	
Body Diode Reverse Recovery Time	t_{rr}	$I_F = 30\text{A}, dI/dt = 100\text{A}/\mu\text{s}$	-	67	-	ns
Body Diode Reverse Recovery Charge	Q_{rr}		-	90	-	nC
Drain-Source Body Diode Characteristics						
Diode Forward Voltage ⁴	V_{SD}	$I_S = 10\text{A}, V_{\text{GS}} = 0\text{V}$	-	-	1.4	V
Continuous Source Current	$T_C = 25^\circ\text{C}$	I_S	-	-	100	A

Notes:

- Repetitive rating, pulse width limited by junction temperature $T_{J(\text{MAX})} = 150^\circ\text{C}$.
- The EAS data shows Max. rating . The test condition is $V_{\text{DD}}=24\text{V}, V_{\text{GS}}=10\text{V}, L=0.1\text{mH}, R_g=25\Omega$
- The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.
- The data tested by pulsed , pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.
- This value is guaranteed by design hence it is not included in the production test.

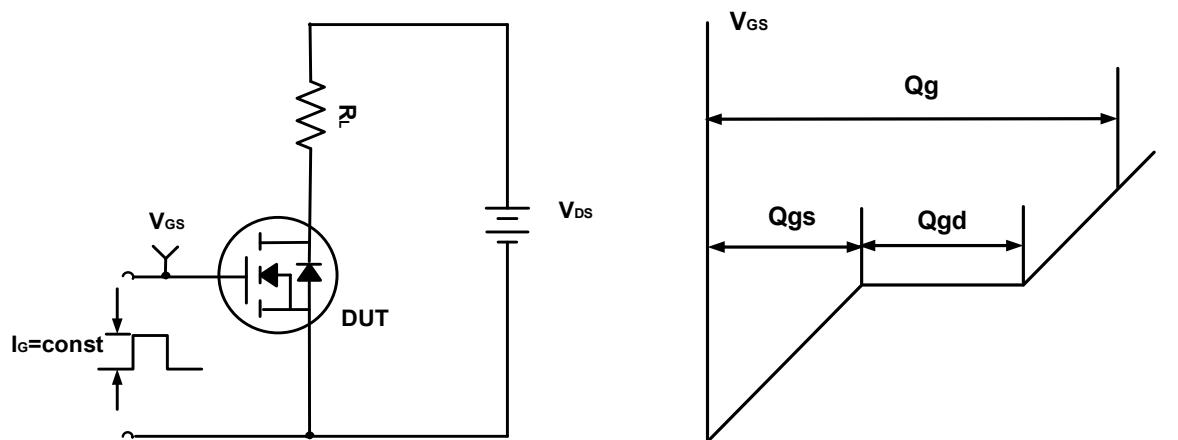
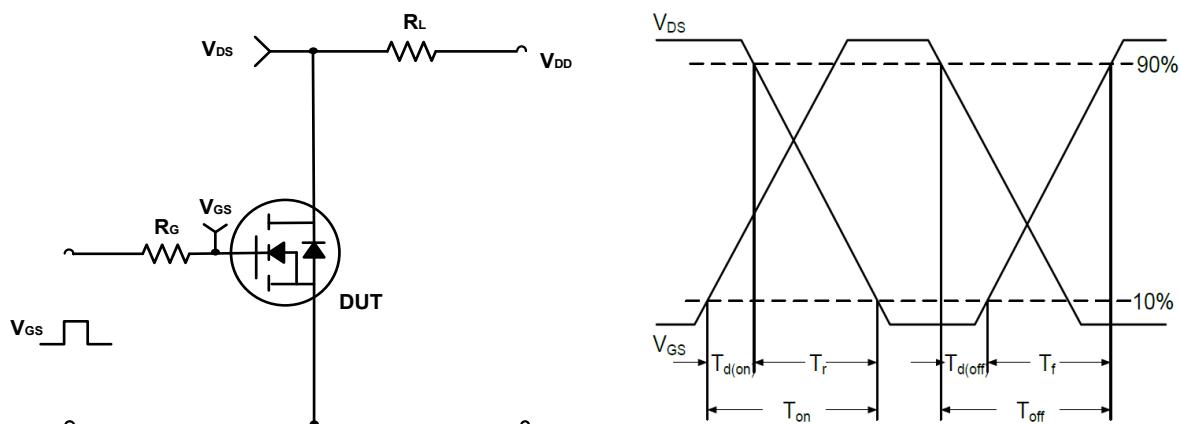
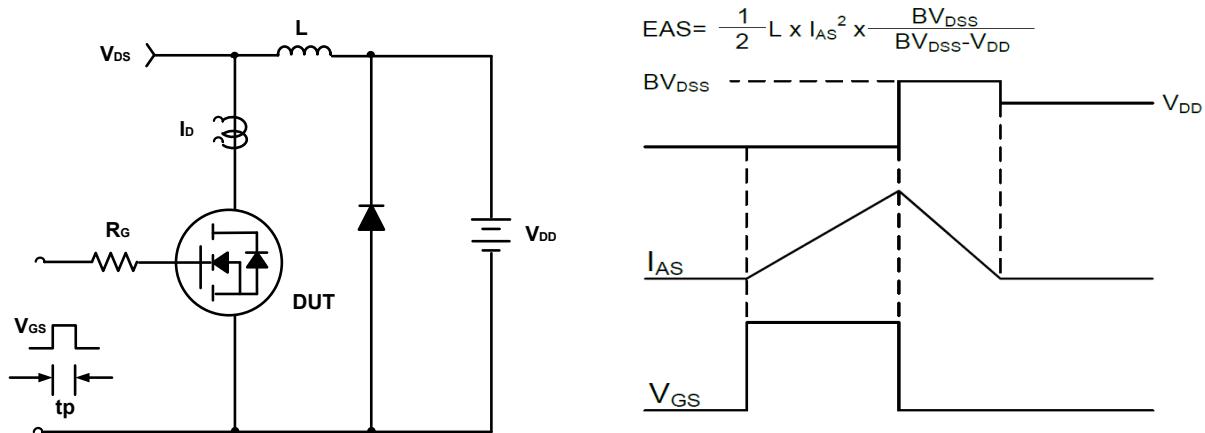
Typical Characteristics

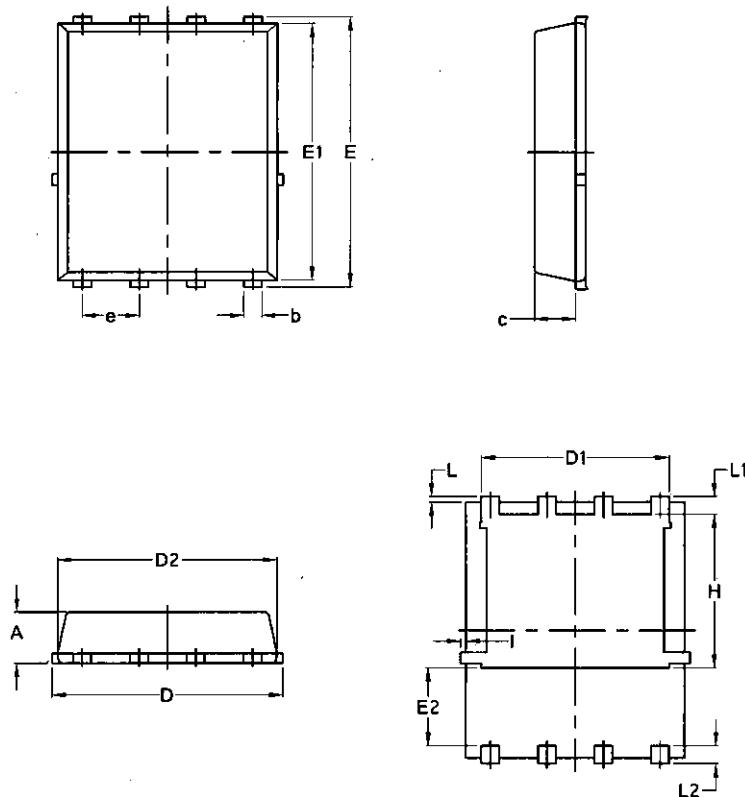




Test Circuit

PDFN5060-8L Package Information


Figure A. Gate Charge Test Circuit & Waveforms

Figure B. Switching Test Circuit & Waveforms

Figure C. Unclamped Inductive Switching Circuit & Waveforms

Package Mechanical Data-PDFN5060-8L(CLIP)- Single


Symbol	Common			
	mm		Inch	
	Mim	Max	Min	Max
A	1.03	1.17	0.0406	0.0461
b	0.34	0.48	0.0134	0.0189
c	0.824	0.0970	0.0324	0.082
D	4.80	5.40	0.1890	0.2126
D1	4.11	4.31	0.1618	0.1697
D2	4.80	5.00	0.1890	0.1969
E	5.95	6.15	0.2343	0.2421
E1	5.65	5.85	0.2224	0.2303
E2	1.60	/	0.0630	/
e	1.27 BSC		0.05 BSC	
L	0.05	0.25	0.0020	0.0098
L1	0.38	0.50	0.0150	0.0197
L2	0.38	0.50	0.0150	0.0197
H	3.30	3.50	0.1299	0.1378
I	/	0.18	/	0.0070